

4ch Multi-Standard HD-Analog Receiver and Codec for Long-Reach DVR System

PR1000K

Rev 0.0

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Product Overview

The PR1000K is a four channel SD/HD video receiver and audio codec which accepts **Any Standard and Resolution of Analog SD/HD video** and guarantees high quality image for **Long-Reach Analog DVR System** applications. It accepts **Single-ended/Differential** analog SD/HD video signal from camera, then **Cable Equalizer** compensates cable attenuation, and SD/HD video decoder converts analog video signal to digital component data. Audio codec converts analog audio signal to digital audio data for recording and converts digital audio data to analog audio signal for playback. For high features of DVR system, the PR1000 provides various information such as video and audio detection, motion detection and camera **Tampering Detection**. The PR1000 also supports **Bi-Directional Coaxial/UTP PTZ** interface so that host can control PTZ camera and receive information from camera with 2-wire serial interface.

▶ Features

♦ Video Decoder

- **★** Multi-Standard HD-Analog and SD video with Auto-Detection
 - All kind of HD-Analog Standard and NTSC/PAL
- **★** Any Resolution of HD-Analog and SD video with Auto-Detection
 - 1080p25/30, 720p25/30/50/60, 480i60, 576i50
- **★** Superior Cable Equalizer for Long-Reach DVR System
- **★** Single/Differential Video Interface for COAX/UTP Cable
- * 4 Video Output Port @ 54/72/108/144/148.5Mhz
- **★** Multi-Channel Time-Multiplexed Video Output with Dual Edge of Clock
- * Arbitrary Scaled Video Output
 - Supports HD1080p to HD720p Scaled output

♦ Motion & Camera Tampering Detection

★ Motion / Blockage / Spray-Painting / Defocusing

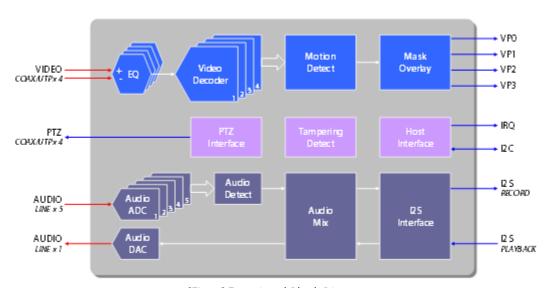
♦ Motion Mask & Private Zone

♦ Audio Codec

- * 5 Audio Input and 1 Audio Output
- * 8/16bit Linear PCM and 8bit u-Law/a-Law
- * I2S Interface for Record and Playback
- * Audio and Mute Detection
- * Multi-Chip Connection

♦ Bi-Directional PTZ Communication

- ♦ I2C Serial Interface
- Low Power Consumption
- **♦** 128 eTQFP



[Fig. 1] Functional Block Diagram

